

Specification at page 7, line 7:

Yet still another aspect of the present invention is an asynchronous FIFO data reading and writing method comprising:

Specification at page 8, line 1:

Still yet another aspect of the present invention is an asynchronous FIFO data reading and writing method comprising:

Specification at page 9, line 20:

A further aspect of the present invention is the asynchronous FIFO data reading and writing method, wherein said error write step and said error read step are formed of a gray code count step.

Respectfully Submitted,


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AR/dlm

Enclosures: Version with markings to show changes made

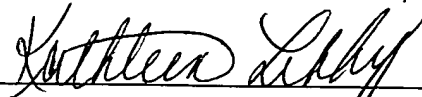
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